

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	((memory adj expander adj (chip or module or unit)) or mxc or hub) near refresh\$3	US-PGPUB; USPAT	OR	ON	2006/03/07 22:29
L2	16	((memory adj expander adj (chip or module or unit)) or mxc or hub) near7 refresh\$3	US-PGPUB; USPAT	OR	ON	2006/03/07 22:52
L3	9	(variable near (resistance or impedance)) near5 splitter	US-PGPUB; USPAT	OR	ON	2006/03/07 22:53
L4	0	(variable near (resistance or impedance)) near5 bus adj splitter	US-PGPUB; USPAT	OR	ON	2006/03/07 22:53
L5	0	(variable near (resistance or impedance)) with bus adj splitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 22:53
S2	58	terminator near5 (signal near reflection)	US-PGPUB; USPAT	OR	ON	2005/11/11 15:01
S3	1	terminator near5 (signal near reflection) with split	US-PGPUB; USPAT	OR	ON	2005/11/11 14:49
S4	1	terminator near5 (signal near reflection) with split\$4	US-PGPUB; USPAT	OR	ON	2005/11/11 14:49
S5	3469	dimmm or (dual adj ((in adj line) or in?line) adj memory adj module)	US-PGPUB; USPAT	OR	ON	2005/11/11 15:02
S6	5	S2 and S5	US-PGPUB; USPAT	OR	ON	2005/11/11 15:02
S7	0	("6862185").URPN.	USPAT	OR	ON	2005/11/11 15:07
S8	16	("4755146" "5172873" "5198965" "5208729" "5297006" "5313097" "5390078" "5861666" "5986882" "5986887" "6262890" "6297960" "6351386" "6421240" "6424532" "6519157").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/11 15:31
S9	4528	reference adj voltage adj generator	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/11 15:32
S10	7717	memory adj2 voltage	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/11 15:31
S11	37	S9 near5 S10	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/11 15:32
S12	1	(reference adj voltage adj generator) with dimmm	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/11 15:33
S13	1	(reference adj voltage adj generator) same dimmm	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/11 15:40
S14	170	((memory adj expander adj chip) or mxc)	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/11 15:50
S15	3	((memory adj expander adj chip) or mxc) with array	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/11 15:49
S16	3	(memory adj expander adj chip)	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/11 15:53
S17	104	intel near2 "8155"	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/11 15:52
S18	1	(memory adj expander adj chip)	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 16:00
S19	1749	controller near5 (memory near array)	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/11 16:04
S20	7	controller near5 (memory near array) same dimmm	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/11 16:04
S21	1310	daughter adj card	US-PGPUB; USPAT	OR	ON	2005/11/11 16:35
S22	678	expan\$5 near4 (memory adj (module or array))	US-PGPUB; USPAT	OR	ON	2005/11/11 16:36
S23	292	expansion near4 (memory adj (module or array))	US-PGPUB; USPAT	OR	ON	2005/11/11 16:36
S24	111	(high\$3 near bandwidth) near7 hub	US-PGPUB; USPAT	OR	ON	2005/11/13 14:28
S25	2	(high\$3 near bandwidth) near7 hub with (benefit\$5 or advantag\$4)	US-PGPUB; USPAT	OR	ON	2005/11/13 14:40

EAST Search History

S26	1	(high\$3 near bandwidth) near7 mxc	US-PGPUB; USPAT	OR	ON	2005/11/13 14:39
S27	0	(high\$3 near bandwidth) near7 hub with dimm	US-PGPUB; USPAT	OR	ON	2005/11/13 14:40
S28	0	(high\$3 near bandwidth) with hub with dimm	US-PGPUB; USPAT	OR	ON	2005/11/13 14:40
S29	8	(high\$3 near bandwidth) with dimm	US-PGPUB; USPAT	OR	ON	2005/11/13 14:41
S30	1	(high\$3 near bandwidth) near9 (direct\$3 near3 (connect\$3 or support\$3 or couple\$3)) with dimm	US-PGPUB; USPAT	OR	ON	2005/11/13 14:42
S31	1	(high\$3 near bandwidth) with (direct\$3 near3 (connect\$3 or support\$3 or couple\$3)) with dimm	US-PGPUB; USPAT	OR	ON	2005/11/13 14:42
S32	1	(high\$3 near bandwidth) with direct\$3 with dimm	US-PGPUB; USPAT	OR	ON	2005/11/13 14:42
S33	1	bandwidth with direct\$3 with dimm	US-PGPUB; USPAT	OR	ON	2005/11/13 14:42
S34	286	hub near5 (advantag\$5 or benefi\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/13 14:43
S35	0	hub near5 (advantag\$5 or benefi\$4) with dimm	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/13 14:43
S36	0	hub with (advantag\$5 or benefi\$4) with dimm	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/13 14:43
S37	4390	reference adj voltage adj generator	US-PGPUB; USPAT	OR	ON	2005/11/13 15:08
S38	184	reference adj voltage adj generator with (memory adj2 (module or chip or device or controller))	US-PGPUB; USPAT	OR	ON	2005/11/13 15:12
S39	3	reference adj voltage adj generator with (memory adj2 (module or chip or device or controller)) and dimm	US-PGPUB; USPAT	OR	ON	2005/11/13 15:13
S40	1	"20040264256"	US-PGPUB; USPAT	OR	ON	2005/11/13 15:29
S41	1	miniature adj integrated adj resistor	US-PGPUB; USPAT	OR	ON	2005/11/13 15:29
S42	1	miniature adj integrated adj resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/13 15:29
S43	28	bus near splitter	US-PGPUB; USPAT	OR	ON	2006/03/07 21:39
S45	0	S43 with (specif\$4 near3 resistance)	US-PGPUB; USPAT	OR	ON	2006/03/07 21:39
S47	0	(bus near splitter) with (specif\$4 near3 resistance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 21:40
S48	0	(bus near splitter) with resistance	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 21:40
S49	1	(bus near splitter) near5 resist\$5	US-PGPUB; USPAT	OR	ON	2006/03/07 22:28